

PATENT NUMBER

U.S. UTILITY Patent Application

85 O.I.P.E. SCANNED <u>IK4</u> Q.A. <u>LEN</u>	PATENT DATE
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APPLICATION NO. 09/986282	CONT/PRIOR F	CLASS 174	SUBCLASS 23	ART UNIT 23 261	EXAMINER I. PATEL
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CANTS Yoshiaki Uematsu
Shinji Marabe

Wiring board and a method for manufacturing the wiring board

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		ISSUE FEE Amount Due Date Paid	
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